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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10050745	FILING DATE 01/16/2002	CLASS 324.3.1	SUBCLASS 151.011	GAU 2888	EXAMINER <i>T. K. Kim</i>
<b>**APPLICANTS:</b> Nagata Junichi; <i>25.38</i> <i>Matt Nguyen</i>					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2001-007673 01/16/2001					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no				ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no				4041P-000033	
Verified and Acknowledged Examiners's initials					
TITLE : Integrate circuit device					

U.S. DEPT. OF COMM /PAT. & TM-PTO-435L (Rev. 12-94)

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<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner	
<b>ISSUE FEE</b>		Total Claims	
Amount Due	Date Paid	Print Claim for O.G.	
		<b>DRAWING</b>	
		Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Primary Examiner	
		<b>PREPARED FOR ISSUE</b>	
		Application Examiner	
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